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Title of the Invention:

BACKUP POWER SUPPLY

Background of the Invention:

5           <Field of the Invention>

The present invention relates to a backup power supply arranged in an apparatus.

<Prior Art>

Conventionally, in an apparatus operating under  
10 connection to a commercial AC power supply and in a  
computer suffering damage such as data loss if the  
commercial AC power supply fails, an uninterruptible  
power supply (UPS) is installed externally and a  
countermeasure for service interruption is taken. The  
15 UPS externally installed generally uses a fixed  
inverter power supply system. The fixed inverter power  
supply system UPS is free of the power supply  
switching operation at the time of service  
interruption and high in stability of the power supply.  
20 However, there are many series stages of converters  
through which the current passes during the period  
from the commercial AC power supply to the load, so  
that the power conversion efficiency is made lower and  
realization of power conservation is difficult.

25           On the other hand, a backup power supply that a

secondary battery and a charge-discharge circuit for it are internally loaded and an external UPS is not required is proposed. As an example thereof, "UPS built-in power supply" in Japanese application patent  
5 laid-open publication No. Hei 09-322433 may be cited. The constitution of a conventional backup power supply is shown in Fig. 10. A commercial power supply 1 is connected to an AC-DC converter 3 and a charge circuit 8 installed in an information processor 2 and a  
10 secondary battery 4 and the input side of a DC-DC converter 7 are connected to the output side of the charge circuit 8. Further, the output side of the DC-DC converter 7 and the output side of the AC-DC converter 3 are connected to each other and connected  
15 to a load 6. Further, a balance control circuit 9 is connected between the AC-DC converter 3 and the DC-DC converter 7.

The operation of this circuit is shown in Fig. 11. (a) shows the stationary state and the commercial AC  
20 power supply 1 supplies 90% of the power necessary to the load via the AC-DC converter 3. Further, the charge circuit 8 supplies the residual 10% of power to the load via the DC-DC converter 7. Furthermore, the secondary battery 4 is charged via the charge circuit  
25 8. On the other hand, (b) shows the operation during

service interruption, and since the commercial power supply 1 fails, the charge circuit 8 and the AC-DC converter cannot operate, though the secondary battery 4 supplies all 100% of power necessary to the load via the DC-DC converter 7.

The aforementioned conventional backup power supply requires three converters such as the AC-DC converter, DC-DC converter, and charge circuit, so that problems arise that the price is high and the volume of the power unit is large.

Further, in this power supply system, the charge circuit operates always in the stationary state and a fixed voltage is applied to the secondary battery. However, when a secondary battery of high energy density such as a Ni-MH secondary battery or a Li ion secondary battery is used, to prevent overcharge, it is necessary to stop the charge circuit when the battery enters the full charge state. However, when the charge circuit is stopped by the aforementioned operation method, a problem arises that the DC-DC converter cannot supply 10% of power.

On the other hand, separately from this, a problem of the power supply capacity for load changes is imposed. This problem is that for example, in a load such as a hard disk device, a current 2 or 3 times of the normal

load current flows at start and seek time. The rated capacity of the AC-DC converter is designed in accordance with the peak load time, so that the capacity of the AC-DC converter is increased and  
5 problems of high cost and difficulty in reduction of the capacity of power unit are imposed.

Summary of the Invention:

An object of the present invention is to solve the  
10 aforementioned problems of the prior art and to provide a backup power supply for realizing decrease in cost and reduction in the power unit.

The present invention is a backup power supply built in an apparatus composed of a power supply  
15 circuit for converting an alternating current received from a commercial AC power supply to a direct current and a load operated by the current generated by the power supply circuit and the backup power supply is structured so as to have at least one AC-DC converter  
20 connected to the commercial alternating current, a load connected to the DC output side of the AC-DC converter, at least one two-way DC-DC converter with one side thereof connected to the DC output side, and a secondary battery connected to the other side of the  
25 two-way DC-DC converter.

And, when a load current is larger than a predetermined peak cut current, the differential current between the load current and the predetermined peak cut current is supplied to the load from the secondary battery via the two-way DC-DC converter and the peak cut operation is performed.

Further, when the load current is smaller than the predetermined peak cut current, the load current is supplied from the AC-DC converter and the secondary battery is charged using the two-way DC-DC converter.

The charging current has an upper limit of a predetermined current and only the current equivalent to the differential current between the predetermined peak cut current and the load current is taken in from the two-way DC-DC converter and the secondary battery is charged, so that the commercial input current is stabilized more.

Further, the backup power supply has a detection means for detecting the charging and discharging currents of the secondary battery, a means for detecting the voltage of the secondary battery, and a circuit for calculating the residual capacity of the secondary battery. The predetermined current is changed according to the residual capacity of the secondary battery, and when the residual capacity of

the secondary battery reduces lower than a predetermined capacity, the peak cut operation is stopped, and even when the residual capacity reduces less than the predetermined capacity at the time of service interruption or at the time of occurrence of a fault of the AC-DC converter, the discharge operation is performed, thus the operability is improved.

Or, to have a function for calculating and displaying the service interruption holding time at the point of time from the residual capacity of the secondary battery and the load current and to have a function for calculating the residual capacity of the secondary battery necessary to ensure a predetermined service interruption holding time at the point of time from the residual capacity of the secondary battery and the load current and performing the peak cut operation within the range having the calculated residual capacity are effective problem solving means.

Further, the voltage at the connection point of the AC-DC converter and the two-way DC-DC converter is higher than the voltage of the secondary battery and when the two-way DC-DC converter is discharged from the secondary battery side, it may be operated as a booster chopper circuit and when the secondary battery is charged, it may be operated as a voltage reduction

chopper circuit.

Further, the DC-DC converter has a means for alternately switching the short-circuit mode for short-circuiting the secondary battery and the inductance by the switching element and the booster mode for ejecting the energy stored in the inductance in the short-circuit mode to the load, a means for detecting the inductance current in the booster mode, and a mean for averaging the inductance current in the booster mode, and only when the result obtained by subtracting a predetermined peak cut current level from the load current is positive, sets the value as a peak cut current command value, compares it with the averaged current mentioned above, and controls the ratio of the short-circuit mode to the booster mode.

Or, the DC-DC converter has a means for detecting the current of the load every a predetermined time and a means for storing the mean value of load currents up to the preceding day, and calculates a mean value of new load currents from the detected load currents and the mean value of load currents up to the preceding day at the same time, stores it in the storage means, and effectively changes the aforementioned predetermined peak cut current from the calculated mean value of new load currents.

In the same way, the DC-DC converter has a means for detecting the current of the load every a predetermined time and a means for storing the mean value of load currents up to the preceding week, and  
5 calculates a mean value of new load currents from the detected load currents and the mean value of load currents up to the preceding week at the same time on the same day of the week, stores it in the storage means, changes the aforementioned predetermined peak  
10 cut current from the calculated mean value of new load currents, thus a peak cut operation corresponding to the load pattern every week can be performed.

Brief Description of the Drawings:

15 Fig. 1 is a block diagram of the backup power supply with a peak cut function of the first embodiment;

Fig. 2 is a circuit block diagram of the two-way DC-DC converter of the first embodiment;

20 Fig. 3 is a block diagram of the charge control circuit of the first embodiment;

Fig. 4 is a block diagram of the discharge control circuit of the first embodiment;

25 Fig. 5 is a block diagram of the peak cut control circuit of the first embodiment;



Fig. 6 is a schematic view showing the current path of the first embodiment;

Fig. 7 is an illustration showing the relation between the load current, the output current of the two-way DC-DC converter, and the SOC of the secondary battery of the first embodiment;

Fig. 8 is a graph showing the relation between the peak cut correction current and the peak cut operable time of the first embodiment;

Fig. 9 is a block diagram of the backup power supply with a peak cut function of the second embodiment;

Fig. 10 is a schematic view of a conventional apparatus built-in backup power supply; and

Fig. 11 is a schematic view showing the operation configuration of a conventional apparatus built-in backup power supply.

#### Description of the Invention:

The first embodiment of the present invention will be explained by referring to Figs. 1 to 8. Fig. 1 is a block diagram showing an embodiment of the present invention. In Fig. 1, an information processor 2 internally has an AC-DC converter 3, a secondary battery 4, a two-way DC-DC converter 5, a load 6, a

load current detector 10, a battery current detector  
15, an SOC calculation circuit 16, a peak cut current  
level setting unit 17, a subtracter 18, a battery  
voltage detection means 19, an output voltage  
5 detection means 21, an operation mode switching  
circuit 22, a charge control circuit 23, a peak cut  
control circuit 24, a discharge control circuit 25, a  
service interruption/fault detection circuit 26, and a  
service interruption holding time calculation circuit  
10 50.

A commercial AC power supply 1 is a commercial AC  
power supply of 100 V or 200 V and connected to the  
AC-DC converter 3 and the service interruption/fault  
detection circuit 26 installed in the information  
15 processor 2. A fault signal is input from the AC-DC  
converter 3 to the service interruption/fault  
detection circuit 26. The output of the service  
interruption/fault detection circuit 26 is input to  
the operation mode switching circuit 22. The output of  
20 the AC-DC converter 3 is a direct current at about 48  
V and connected to the two-way DC-DC converter 5 and  
the load 6. The load current detector 10 for detecting  
the current of the load 6 is connected to the input  
side of the load. To the load side of the two-way DC-  
25 DC converter, the output voltage detection means 21

for detecting an voltage is connected and the output thereof is input to the discharge control circuit 25. The secondary battery 4 is connected to the two-way DC-DC converter 5. When the secondary battery 4 is, 5 for example, a nickel-hydrogen battery having a constitution of serial connection of 15 cells, the terminal voltage of the secondary battery is about 18 V.

Between the secondary battery 4 and the two-way 10 DC-DC converter 5, the battery current detector 15 for detecting the current between them is connected. Further, the battery voltage detection means 19 for detecting the voltage of the secondary battery 4 is connected between them. Both the outputs of the 15 battery current detector 15 and the battery voltage detection means 19 are input to the SOC calculation circuit 16 and the charge control circuit 23 and the output of the battery current detector 15 is input to the peak cut control circuit 24 and the discharge 20 control circuit 25.

A battery SOC 30 which is an output of the SOC calculation circuit 16 is connected to the peak cut current level setting unit 17, the charge control circuit 23, and the service interruption holding time 25 calculation circuit 50. The peak cut current level

setting unit 17 is connected to the subtracter 18. Further, the output of the load current detector 10 is input to the subtracter 18 and the service interruption holding time calculation circuit 50. A  
5 charge-discharge current command value 20 which is an output of the subtracter 18 is input to the operation mode switching circuit 22, the charge control circuit 23, and the peak cut control circuit 24.

Drive signals which are outputs of the charge  
10 control circuit 23, the peak cut control circuit 24, and the discharge control circuit 25 are input to a drive signal switching means 29. Further, the output of the operation mode switching circuit is also input to the drive signal switching means 29. A drive signal  
15 27 which is an output of the drive signal switching means 29 is input to the two-way DC-DC converter 5. The output of the service interruption holding time calculation circuit 50 is output to the load 6.

Fig. 2 shows a constitution diagram of the two-way  
20 DC-DC converter 5. In Fig. 2, the same numerals are assigned to the same components as those shown in Fig. 1. Numeral 11 indicates a smoothing condenser, 12 an inductance, 13a and 13b power MOSFET, 14 a smoothing condenser, and 28 a gate drive circuit.

25 In Fig. 2, both ends of the secondary battery 4

are connected to the smoothing condenser in the two-way DC-DC converter 5. To the positive pole of the terminals of the smoothing condenser 11, one end of the inductance 12 is connected and the other end of the inductance 12 is connected to the source of the power MOSFET 13b and the drain of the power MOSFET 13a. The drain of the power MOSFET 13b is connected to the positive side of the smoothing condenser 14 and the source of the power MOSFET 13a is connected to the negative side of the smoothing condenser 11 and the smoothing condenser 14. The drive signal 27 is input to a gate drive signal. Further, the output of the gate drive circuit 28 is connected to the gates of the power MOSFET 13a and the power MOSFET 13b. Both ends of the smoothing condenser 14 are connected to the load 6 outside the two-way DC-DC converter 5.

Fig. 3 is a control block diagram showing the inside of the charge control circuit 23 shown in Fig. 1. Numeral 32 indicates a charge voltage control circuit, 33 a maximum output means, 34a a PWM comparator, 35a a triangular wave generation means, 36 a charging current control circuit, 37 a battery voltage command value, 38a a positive-negative inversion means, 39 a variable limiter, 43a a multiplier, and 44a a time lag of first order element.

The output of the battery voltage detection means 19 is input to the charge voltage control circuit 32 inside the charge control circuit 23. Further, the battery voltage command value 37 is input to the charge voltage control circuit 32. On the other hand, the output of the battery current detector 15 is input to the multiplier 43a inside the charge control circuit 23. The output of the multiplier 43a is input to the time lag of first order element 44a. The output of the time lag of first order element 44a is input to the charge current control circuit 36. The charge-discharge current command value 20 is input to the positive-negative inversion means 38a inside the charge control circuit 23. The output of the positive-negative inversion means 38a is input to the variable limiter 39. The battery SOC 30 is input to the variable limiter 39. The output of the variable limiter 39 is input to the charge current control circuit 36.

The output of the charge voltage control circuit 32 and the output of the charge current control circuit 36 are input to the maximum value output means 33. The output of the maximum value output means 33 is input to the PWM comparator 34a. Further, the output of the triangular wave generation means 35a is input

to the PWM comparator 34a. The output of the PWM comparator 34a is connected to the drive signal switching means 29 outside the charge control circuit 23 and the multiplier 43a.

5        Fig. 4 is a control block diagram showing the inside of the discharge control circuit 25 shown in Fig. 1. The output of the output voltage detection means 21 is input to the output voltage control circuit 40 inside the discharge control circuit 25.

10        Further, the output of the output voltage command value 42 is also input to the output voltage control circuit 40. The output of the output voltage control circuit 40 is input to the output current control circuit 41. The output of the battery current detector

15        15 is input to the output current control circuit 41 inside the discharge control circuit 25 via the positive-negative inversion means 38b. The output of the output current control circuit 41 is input to the PWM comparator 34b. Further, the output of the

20        triangular wave generation means 35b is input to the PWM comparator 34b. The output of the PWM comparator 34b is output to the drive signal switching means 29 outside the discharge control circuit 25.

      Fig. 5 is a control block diagram showing the

25        inside of the peak cut control circuit 24 shown in Fig.

1. In Fig. 5, the same numerals are assigned to the same components as those shown in Figs. 1, 2, 3, and 4. Numeral 34c indicates a PWM comparator, 35c a triangular wave generation means, 38c a positive-negative inversion means, 43b a multiplier, 44b a time lag of first order element, 45 a peak cut current control circuit, 46 a limiter, and 47 an inverter.

Next, the connection shown in Fig. 5 will be explained. The output of the battery current detector 15 is input to the positive-negative inversion means 38c and the output of the positive-negative inversion means 38c is input to the multiplier 43b. The output of the multiplier 43b is input to the time lag of first order element 44b and the output is input to the peak cut current control circuit 45. On the other hand, the charge-discharge current command value 20 is input to the limiter 46. The output of the limiter 46 is input to the peak cut current control circuit 45. The output of the peak cut current control circuit 45 is input to the PWM comparator 34c. The output of the triangular wave generation means 35c is input to the PWM comparator 34c. The output of the PWM comparator 34c is output to the inverter 47 and the drive signal switching means 29 outside the peak cut control circuit 24. The output of the inverter 47 is input to



the multiplier 43b.

Next, the operation of this embodiment will be explained. When the load 6 is a light load, as shown in Fig. 6(a), the secondary battery is charged. In Fig. 1, the commercial AC power supply 1 performs the power supply operation to the load 6 via the AC-DC converter 3 and charges the secondary battery 4 from the load side of the AC-DC converter 3 via the two-way DC-DC converter 5. In this case, the commercial AC power supply 1 and the AC-DC converter 3 are sound, so that the output of the service interruption/fault detection circuit 26 is on the low level and moreover, the load current which is an output of the load current detector 10 is smaller than the output of the peak cut current level setting unit 17, so that the output of the subtracter 18 is a negative value. Therefore, the output of the operation mode switching circuit 22 is "Charge" and the drive signal switching means 29 is switched so as to set the output of the charge control circuit 23 to the drive signal 27.

Next, the operation of the two-way DC-DC converter in the charging stage will be explained. In the charging state, the two-way DC-DC converter turns the power MOSFET 13b on or off and controls the time ratio which is a ratio between the on-period and the off-

period, thereby controls the current flowing into the secondary battery 4.

In Fig. 2, the voltage of the smoothing condenser 14 is about 48 V because it is an output of the AC-DC converter and the terminal voltage of the secondary battery 4 is about 18 V. Therefore, when the power MOSFET 13b is turned on, the current flows in the secondary battery 4 from the power MOSFET 13b via the inductance 12 and charges the secondary battery 4. When the power MOSFET 13b is turned off, the current flowing in the inductance 12 until then flows back via the body diode of the power MOSFET 13a. Then, the time ratio of the power MOSFET 13b is controlled, thus the charging current flowing in the secondary battery can be controlled.

Further, at this time, by flowing the current back in the body diode of the power MOSFET 13a, the loss in energy is reduced, so that there is a synchronous rectification art available for synchronizing the power MOSFET 13a during the body diode current supply period and turning it on and the synchronous rectification can be used also in this embodiment.

In this charging stage, the charging voltage and charging current are controlled by the charge control circuit 23. The voltage of the secondary battery 4 is

input to the charge control circuit 23 by the battery voltage detection means 19. In Fig. 3, the secondary battery voltage is compared with the battery voltage command value 37 by the battery voltage control  
5 circuit 32 and errors thereof are amplified and output. On the other hand, the charging current flowing from the side of the AC-DC-converter 3 into the two-way DC-DC converter 5 is a current passing the inductance 12 during the period that the power MOSFET 13b is on in  
10 Fig. 2. Therefore, the charging current detects the current waveform of the inductance 12 by the battery current detector 15, calculates the product of the current and the output signal of the PWM comparator 34a during the on-period of the power MOSFET 13b by  
15 the multiplier 43a, and further averages and obtains by the time lag of first order element 44a.

This charging current is input to the charging current control circuit 36. The charging current command value is decided as shown below. As shown in  
20 Fig. 1, the difference (negative value) between the load current and the peak cut current level is set to a positive value as a charge-discharge current command value 20 via the positive-negative inversion means 38a inside the charge control circuit 23 and input to the  
25 variable limiter 39.

On the other hand, the voltage of the secondary battery 4 and the charging current are input to the SOC calculation circuit 16 and the residual capacity (SOC) of the secondary battery 4 is calculated. The  
5 battery SOC 30 is input to the variable limiter 39 inside the charge control circuit 23. In the variable limiter 39, the maximum value of the charging current command value is changed by the battery SOC 30. With respect to the maximum value, for example, when the  
10 SOC is less than 80%, the charging current command value is set to 2C, and when the SOC is from 80% to less than 100%, it is set to 1C, and when the SOC is 100%, it is set to 0. The output of the variable limiter 39 is input to the charging current control  
15 circuit 36.

The outputs of the charging current control circuit 36 and the charging voltage control circuit 32 are input to the maximum value output means 33 and the greater one among them is output to the PWM comparator  
20 34a. The PWM comparator 34a compares the output of the maximum value output means 33 with the output of the triangular wave generation means 35a and outputs the PWM signal. The PWM signal is changed to the drive signal 27 by the drive signal switching means 29 and  
25 executes PWM control for the two-way DC-DC converter.

The charge control of this embodiment charges a charging current equivalent to the difference between the load current and the peak cut level within the range of predetermined charging currents depending on the SOC of the secondary battery.

As mentioned above, in this embodiment, when the residual capacity of the secondary battery is little, so long as it does not exceed the rated capacity of the AC-DC converter, a large current as far as possible is taken in on the two-way DC-DC converter side, thus the secondary battery is rapidly charged and preparations can be made at high speed so as to ensure a sufficient backup time at the time of service interruption.

Next, discharge control at the time of service interruption or at the time of occurrence of a fault of the AC-DC converter will be explained. At the time of occurrence of service interruption, as shown in Fig. 6(c), power is supplied to the load from the secondary battery via the two-way DC-DC converter. In Fig. 1, when the service interruption/fault detection circuit 26 detects a service interruption of the commercial AC power supply 1 or a fault of the AC-DC converter 3, the output of the service interruption/fault detection circuit 26 goes high and the operation mode switching

circuit 22 switches the operation mode to "Discharge" immediately. In this case, a signal output from the discharge control circuit 25 by the drive signal switching means 29 is selected as a drive signal 27.

5       Next, the operation of the two-way DC-DC converter 5 in the discharge state shown in Fig. 2 will be explained. In the discharge state, unlike the charge state, the two-way DC-DC converter turns the power MOSFET 13a on or off and controls the time ratio which  
10       is a ratio between the on-period and the off-period, thereby controls the voltage supplied to the load 6. Assuming the voltage of the secondary battery as about 18 V, it is necessary to output 48 V to be supplied to the load to the smoothing condenser 14.

15       Then, when the power MOSFET 13a is turned on and the secondary battery 4 is short-circuited via the inductance 12, the current flowing in the inductance 12 increases with time. At this time, when the power MOSFET 13a is turned off, the current flowing in the  
20       inductance 12 is output to the smoothing condenser 14 via the body diode of the power MOSFET 13b. Then, when the time ratio of the power MOSFET 13a is controlled, the current flowing in the body diode of the power MOSFET 13b is controlled and after all, the voltage  
25       supplied to the load 6 can be controlled stably.

The operation of the discharge control circuit 25 will be explained by referring to Fig. 4. In Fig. 4, the voltage detected by the output voltage detection means 21 is input to the output voltage control  
5 circuit 40 inside the discharge control circuit 25 and compared with the output voltage command value 42 and an error is amplified. This output becomes an output current command value and is input to the output  
10 current control circuit 41. On the other hand, the discharging current from the secondary battery which is detected by the battery current detector 15 is inverted in sign by the positive-negative inversion means 38b because the charging direction is set  
15 positive, input to the output current control circuit 41, and compared with the output current command value and an error is amplified. This output is input to the PWM comparator 34b and compared with the triangular  
20 wave which is an output of the triangular wave generation means 35b. This comparison result becomes a PWM signal, is input to the gate drive circuit 28 shown in Fig. 2 as a drive signal 27, and drives the power MOSFET 13a and 13b. By doing this, the two-way DC-DC converter 5 is controlled so as to make the  
25 voltage of the smoothing condenser 14 equal to the output voltage command value. As stated in above,

since the charge and the discharge are controlled, a function as USP can be satisfied.

Next, peak cut control will be explained. At the time of peak load, as shown in Fig. 6(b), the operation of power supply to the load from the AC-DC converter and the two-way DC-DC converter is performed. Namely, the two-way DC-DC converter 5 connected as shown in Fig.1 alternately switches the short-circuit mode for short-circuiting the secondary battery 4 and the inductance 12 by the switching element and the booster mode for ejecting the energy stored in the inductance 12 in the short-circuit mode to the load 6. Further, the converter 5 has a means for detecting the inductance current in the booster mode and a mean for averaging the inductance current in the booster mode, and only when the result obtained by subtracting a predetermined peak cut current level from the load current is positive, sets the value as a peak cut current command value, compares it with the averaged current, and controls the ratio of the short-circuit mode to the booster mode. Hereunder, the aforementioned will be explained concretely.

In Fig. 1, when a load current exceeding the current of the peak cut current level setting unit 17 flows, the output of the service interruption/fault



detection circuit 26 goes low and the output of the subtracter 18 becomes a positive value, so that the output of the operation mode switching circuit 22 is set to "Peak cut operation" and the drive signal  
5 switching means 29 is switched so that the output signal from the peak cut control circuit 24 is selected as a drive signal 27.

In Fig. 5, the discharging current detected by the battery current detector 15 is inverted in sign by the  
10 positive-negative inversion means 38b because the charging direction is set positive and input to the multiplier 43b. The output pulse signal of the PWM comparator 34c is inverted by the inverter 47 and a digital signal of 0 or 1 is input to the other side of  
15 the multiplier 43b. As a result, when the output of the PWM comparator 34c is high, the power MOSFET 13a shown in Fig. 2 is ON and the output of the multiplier 43b becomes 0. On the other hand, when the output of the PWM comparator 34c is low, the power MOSFET 13a is  
20 OFF and the output of the multiplier 43b becomes an input of the inverter 38c.

Therefore, by the multiplier 43b, among the currents flowing in the inductance 12, the current in the short-circuit mode which is the on-period of the  
25 power MOSFET 13a is removed and only the current in

the booster mode which passes the body diode of the power MOSFET 13b is output. The booster mode current is averaged by the time lag of first order element 44b and input to the peak cut current control circuit 45.

5       The charge-discharge current command value 20 is the difference between the load current and the peak cut level, which is a discharging current command value in this case. This command value is restricted by the limiter 46 for cutting the negative side, so  
10       that only when the load current is larger than the peak cut current level, it passes the limiter 46 and is input to the peak cut current control circuit 45. The output of the peak cut current control circuit 45 is input to the PWM comparator 34c and compared with  
15       the triangular wave which is an output of the triangular wave generation means 35c and the PWM signal is input to the two-way DC-DC converter 5 via the drive signal switching means 29.

20       Under the peak cut control, the differential current between the load current and the peak cut current level is output from the two-way DC-DC converter by the control system mentioned above. As a result, the output current from the AC-DC converter is constant on the peak cut current level.

25       In Fig. 7, the aforementioned waveforms of each

unit under the peak cut control and at the time of charge are shown. When the load current is higher than the peak cut current level, a discharging current is output from the two-way DC-DC converter 5 and the difference is corrected. When the load current becomes lower than the peak cut current level thereafter, the SOC of the secondary battery is lowered due to discharge, so that the charging operation is performed. Further, when the SOC of the secondary battery becomes lower than 80% which is a preset value, the peak cut level is changed and the peak cut correction amount is reduced. Further, the peak cut control is executed within the range of the SOC of the secondary battery from 100% to 50% and for service interruption correction, 50% or more of the SOC of the secondary battery is always charged.

Fig. 8 shows, as an example, the relation between the peak cut correction current and the peak cut operable time. For the secondary battery, the rated load can be corrected for six minutes during service interruption at an SOC of 50%. A parameter is the SOC of the secondary battery at start of the peak cut operation. The peak cut correction current on the transverse axis is an amount derived from standardizing the current output from the secondary

battery via the two-way DC-DC converter by the rated current. As a result, assuming the peak cut correction current as 20% when the SOC is 100%, the rated load can be corrected continuously for 0.5 hours, while  
5 when the SOC is 55%, the rated load can be corrected continuously only for 0.05 hours.

Therefore, in this embodiment, the peak cut level is changed depending on the SOC of the secondary battery, and the service interruption correction  
10 capacity necessary at its minimum is always kept, and the most suitable peak cut value is dynamically changed, thus the operation is performed.

Next, the operation of the service interruption holding time calculation circuit 50 shown in Fig. 1  
15 will be explained. The service interruption holding time calculation circuit 50 reads the battery SOC 30 and load current and calculates the service interruption holding time. The battery SOC 30 outputs the calculation results to the load 6. By doing this,  
20 on the CRT included in the load 6 or an liquid crystal monitor, the service interruption holding time can be displayed. Since the service interruption holding time can be displayed, the use condition of the battery can be recognized always, so that an operation method  
25 using a constant peak cut current level can be

realized easily.

Further, in this embodiment, the drawing shows a state that the AC-DC converter 3, the two-way DC-DC converter 5, and the secondary battery 4 are used one each respectively or as a one system. To eliminate damage to the load at the time of a fault of the converters, the embodiment can correspond also to a power supply for an  $n+1$  unit parallel redundant constitution constituting a redundant system as parallel connection of a plurality of units of the AC-DC converter 3. In this case, a plurality of units of an integrated unit of the two-way DC-DC converter and the secondary battery are connected in parallel, thus the reliability at the time of occurrence of a fault, maintenance and inspection, or exchange of the two-way DC-DC converter or the secondary battery can be enhanced. Or, a constitution that a plurality of only two-way DC-DC converters are connected in parallel and a one system of secondary battery is used or a constitution that a plurality of systems of secondary batteries and one two-way DC-DC converter are used is also available.

Next, the second embodiment of the present invention will be explained. In Fig. 9, the same numerals are assigned to the same components as those

shown in Fig. 1. Additionally, a memory 48 and a load  
current pattern setting unit 49 are provided. The  
connection configuration of the components other than  
the aforementioned shown in Fig. 9 is the same as that  
5 shown in Fig. 1. The memory 48 is connected to the  
load current detector 10 and the load current pattern  
setting unit 49 is connected to the memory 48. The  
output of the load current pattern setting unit 49 is  
input to the peak cut current level setting unit 17.

10       Next, the operation will be explained. The  
variation of the load current is detected by the load  
current detector 10 and recorded in the memory 48. For  
example, one day is divided every a fixed period such  
as every one minute or one second and load currents  
15 are sampled and stored at a predetermined address of  
the memory 48 as mean value data thereof. At the same  
time on the next day, a mean value of the mean value  
data of load currents at the same time up to the  
preceding day and the present load current is  
20 calculated newly and the mean value data is rewritten.  
In this way, the mean value of daily load currents in  
each region is stored. Or, a memory of one week is  
prepared and the mean value of similar load currents  
at the same time on the same day of the week may be  
25 stored. As a result, by the load current pattern

setting unit 49, an average load current pattern in a one-day period or one-week period of the load 6 is automatically prepared.

5 In this embodiment, this load current pattern is reflected on the peak cut current level. Namely, in the period that a comparatively large load current is continuous, the peak cut current level is set comparatively high, thus the discharge rate from the secondary battery can be suppressed. Or, on the basis  
10 of the capacity of the secondary battery, a most suitable peak cut current level can be set every time from the aforementioned load current pattern.

By use of the constitution of this embodiment, for example, many kinds of backup power supplies having  
15 different load capacities can be structured by the same hardware and the manufacturing cost can be reduced. Further, a most suitable peak cut level according to the load can be automatically set and manual initial setting is not necessary, so that when  
20 a user only connects the backup power supply of the present invention, the peak cut operation is functioned, thus the operability is improved.

According to the present invention, the peak cut operation discharged from the secondary battery at the  
25 time of peak load is executed, thus the capacity of

the AC-DC converter is reduced and low price and reduction in the capacity of the power unit can be realized. Further, overcharge can be prevented. Furthermore, the peak cut level of the present  
5 invention can be made changeable as a set value, thus the same hardware can be used for a different load and the manufacturing cost can be reduced. Further, by display of the service interruption holding time, even in an operation configuration having a constant peak  
10 cut amount, the reliability is enhanced.